



H/4 Supp
500.39252X00
9/24/02
RECEIVED
SEP 24 2002
TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: INOUE et al.
Serial No.: 09/698,186
Filed: October 30, 2000
For: SEMICONDUCTOR DEVICE AND METHOD FOR PRODUCING THE SAME
Group: 2811
Examiner: P. Costanzo

SUPPLEMENTAL RESPONSE

Assistant Commissioner
for Patents
Washington, D.C. 20231

September 20, 2002

Sir:

In supplement to the Response to Restriction/Election Requirement filed on September 3, 2002, please amend the above-identified application as follows:

In the Claims:

Please add new claims 25-38 as follows:

- 25. A method according to claim 19,
wherein said wafer has a circuit electrode;
wherein said step of forming an electrically insulating layer comprises forming a first electrically insulating layer on said wafer with a part of said circuit electrode exposed from said first insulating layer;
wherein said electrically insulating layer forming step by printing by use of a mask includes a step of forming, by printing by use of a mask, a second electrically